

Fig. 1

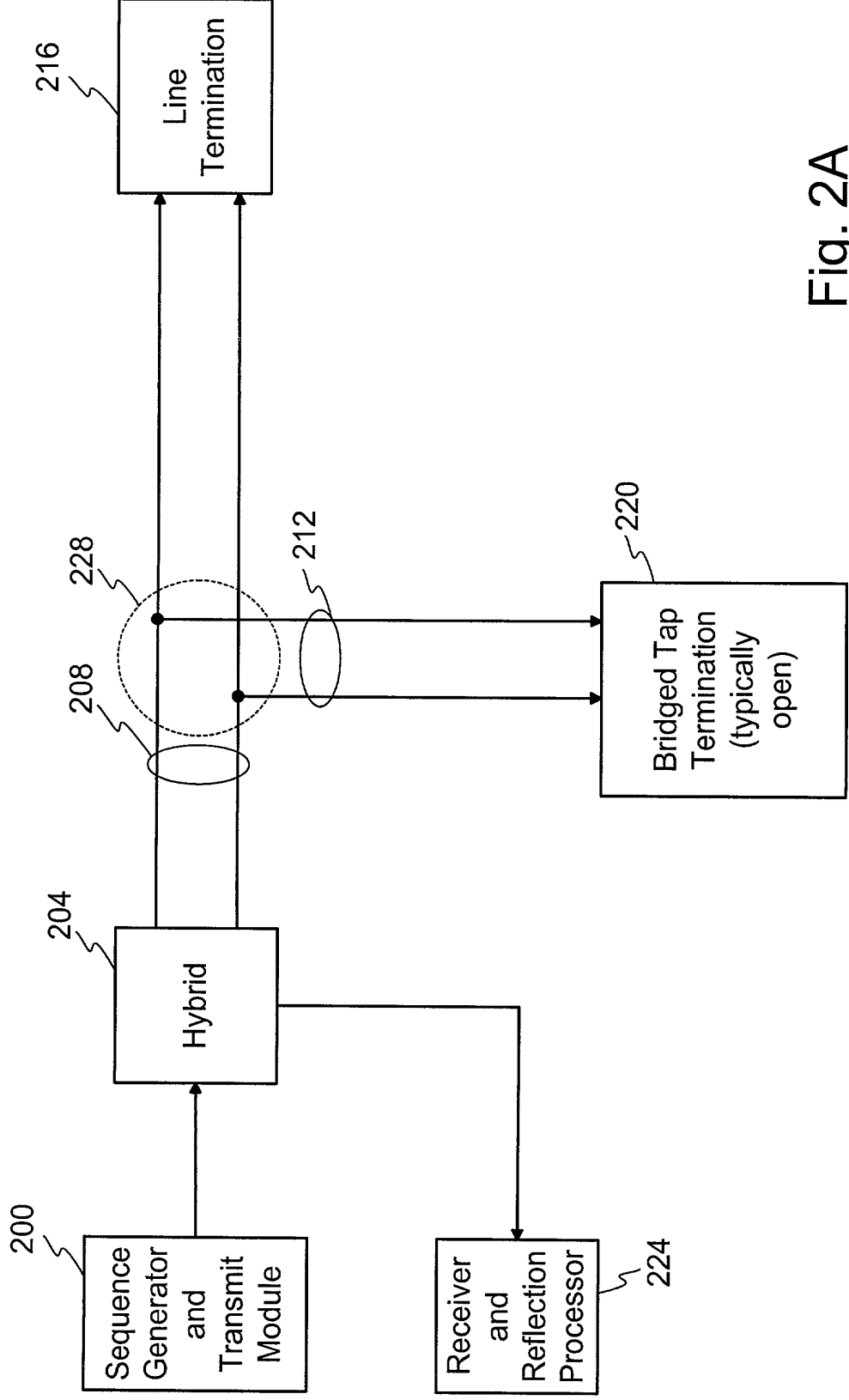


Fig. 2A

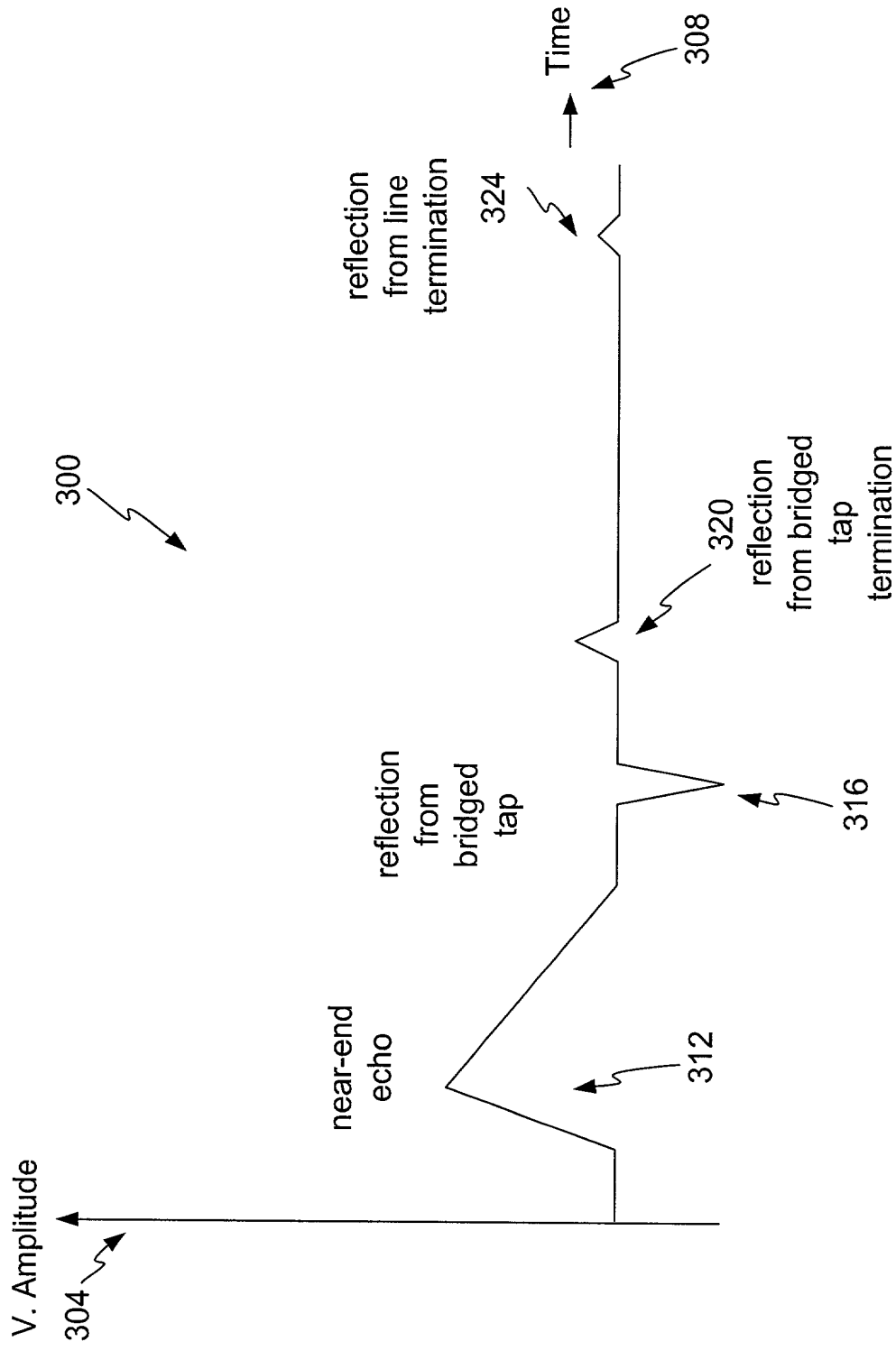


Fig. 2B

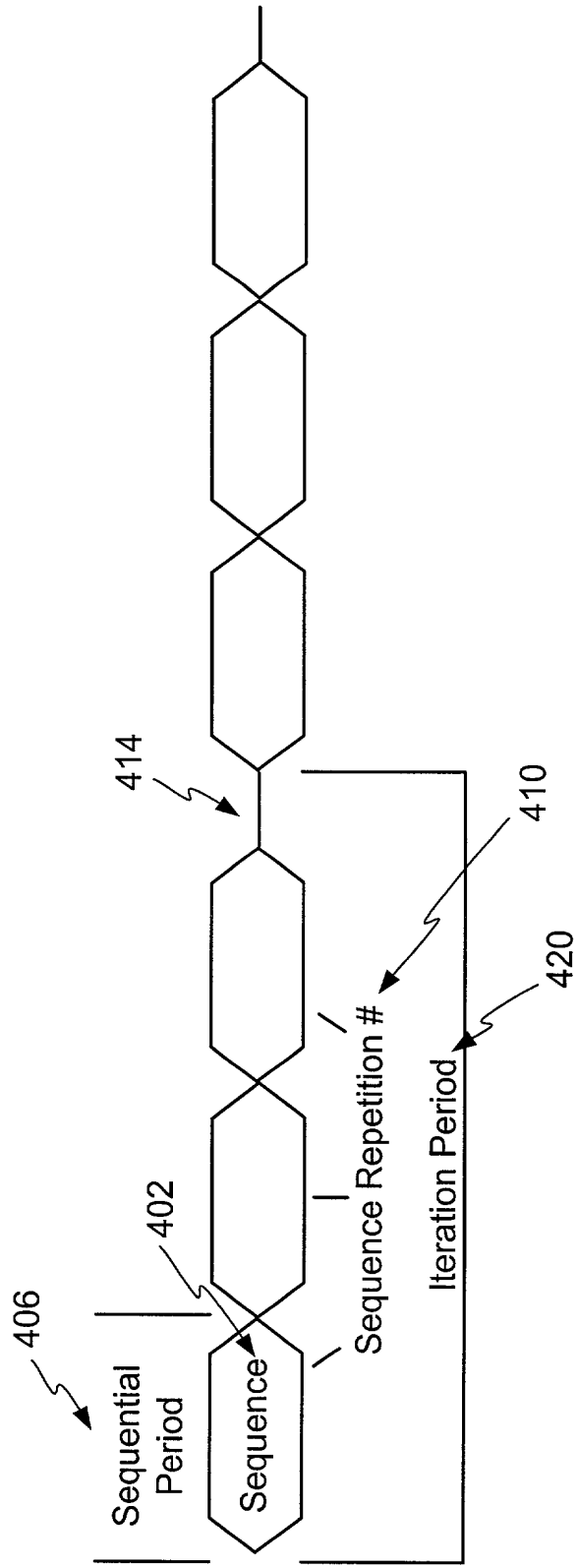


Fig. 3

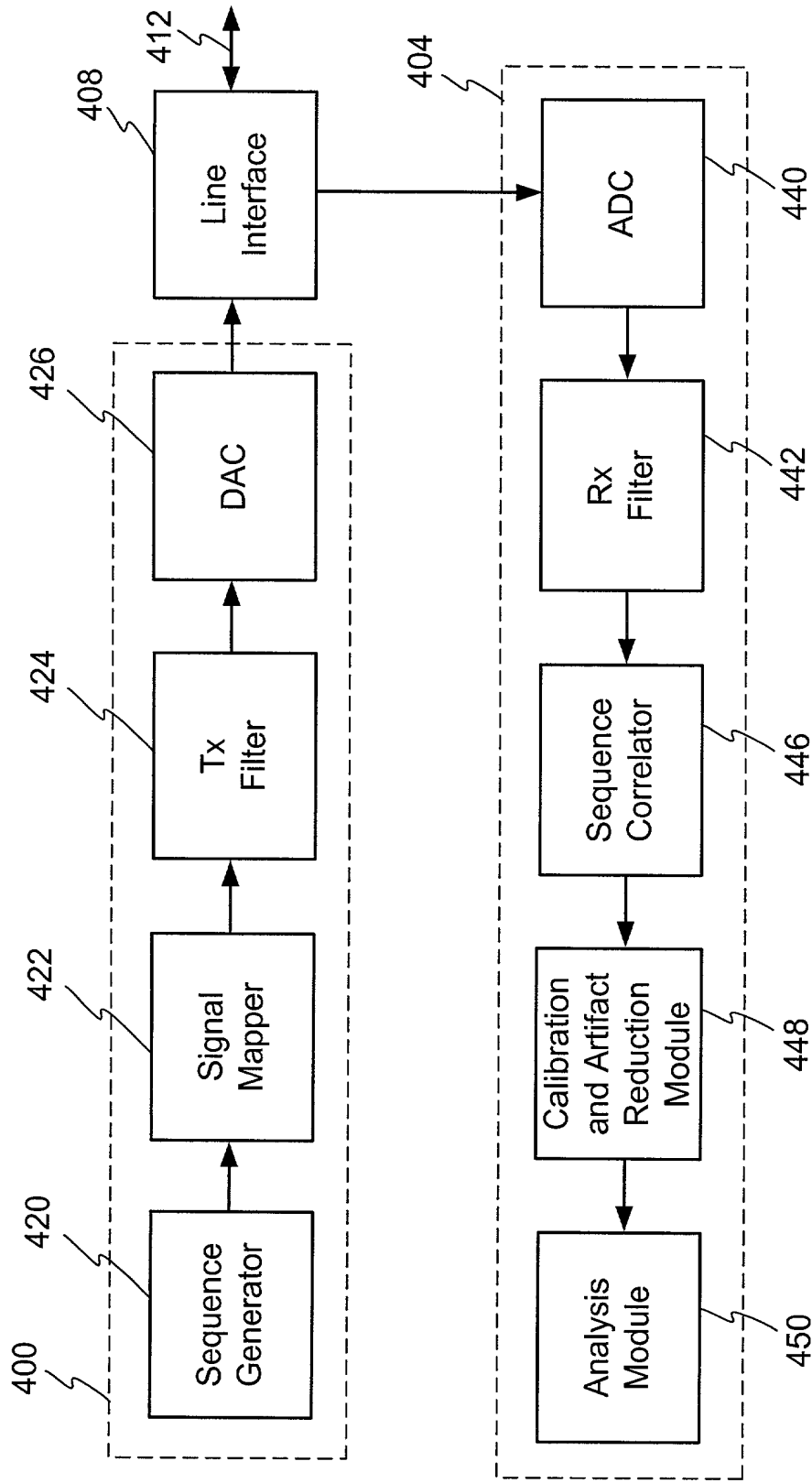


Fig. 4

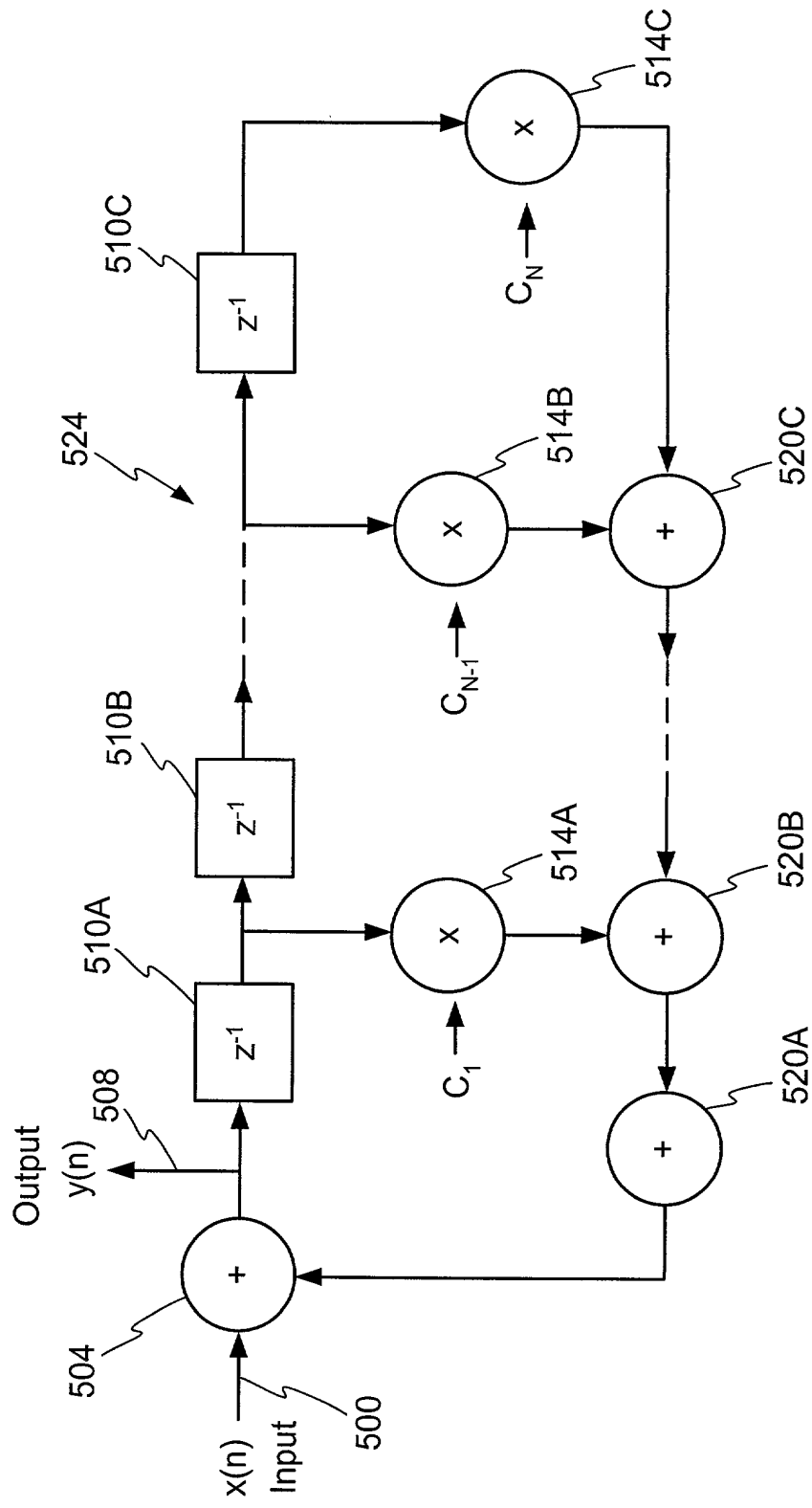


Fig. 5

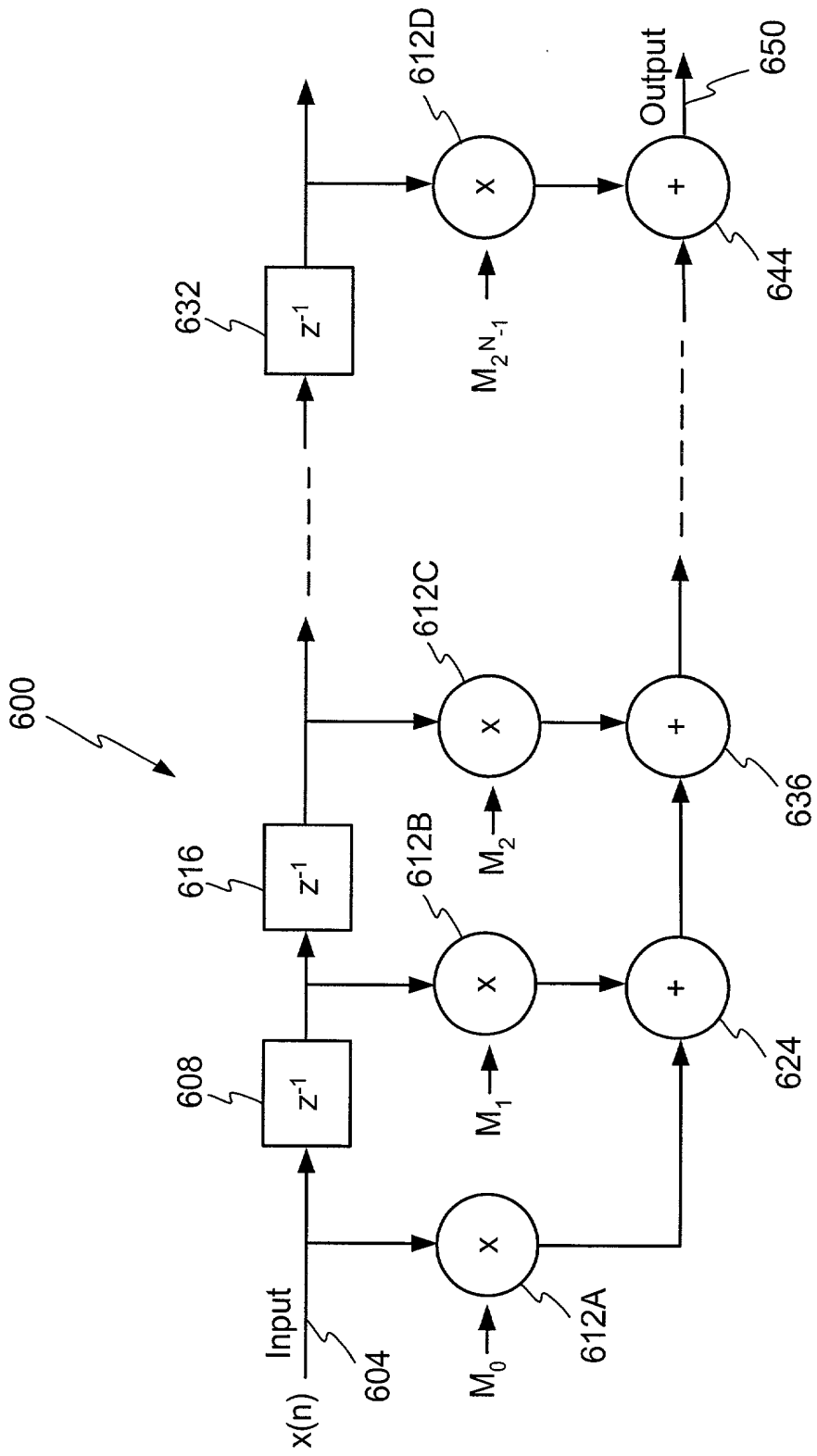


Fig. 6

FIG. 7 is a block diagram of a receiver system 700. The receiver system 700 includes a receive signal input 704, a multiplier 708, a summer 718, and an output 720. The receive signal input 704 provides a receive signal to the multiplier 708. The multiplier 708 also receives a correction signal C(n) from a summer 718. The multiplier 708 outputs a signal to the summer 718. The summer 718 calculates the sum of the signal from the multiplier 708 and a feedback signal from the output 720. The output 720 provides the feedback signal to the summer 718.

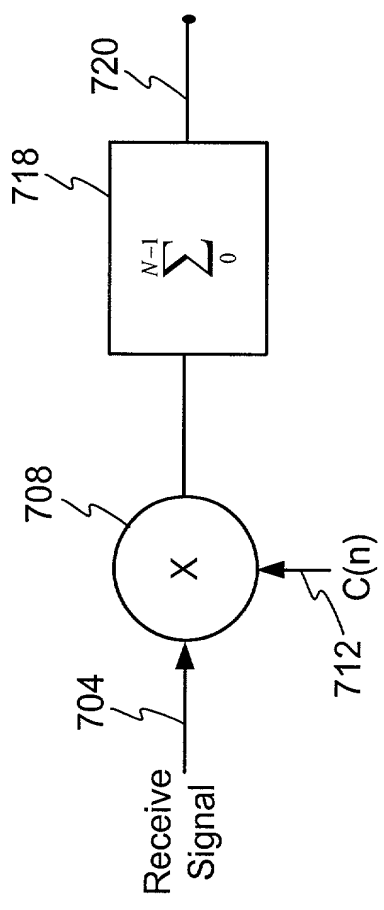


Fig. 7



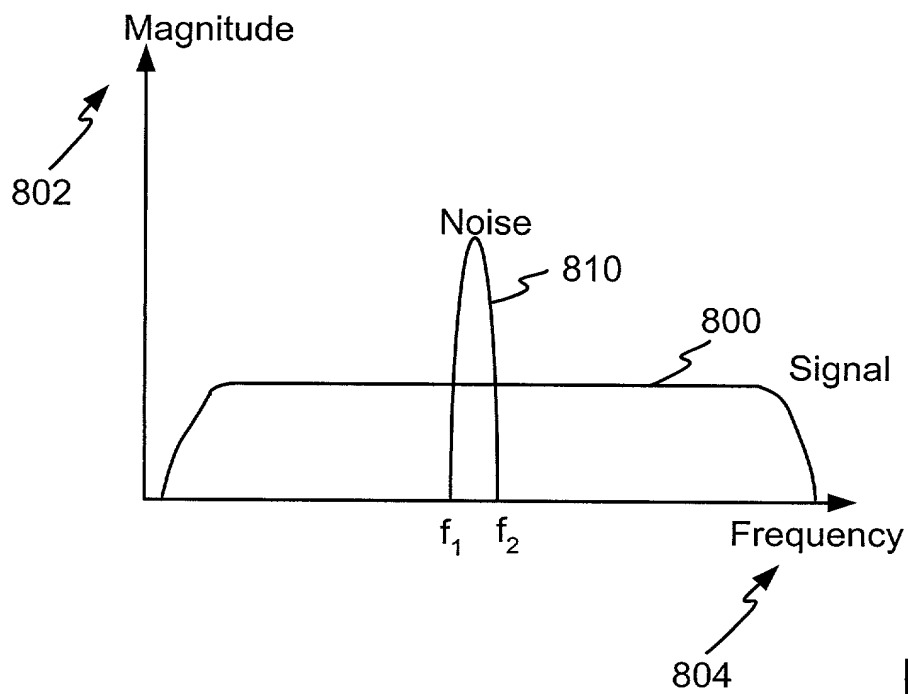


Fig. 8A

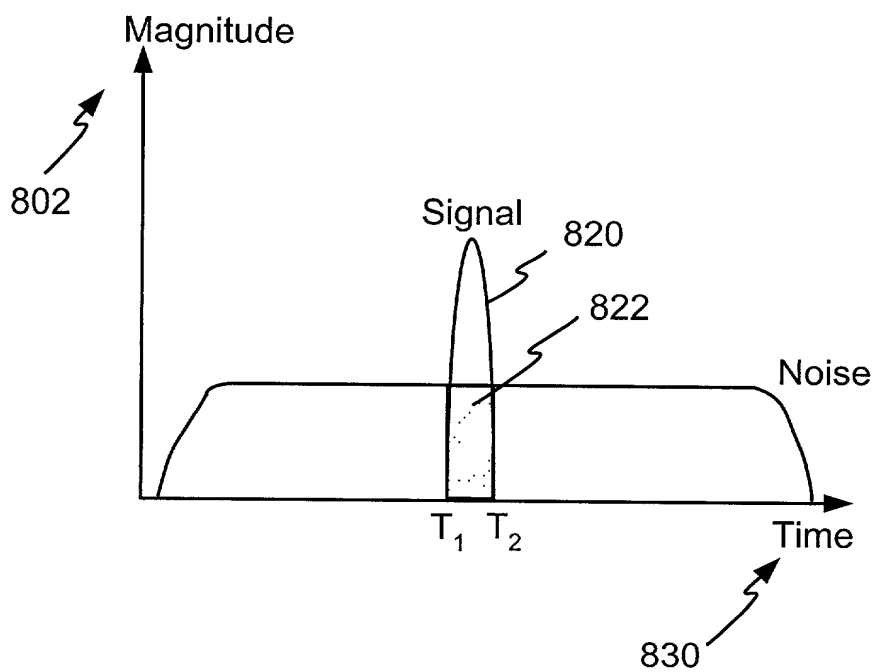


Fig. 8B

FIG. 9 is a graph of an M-sequence of length 15.

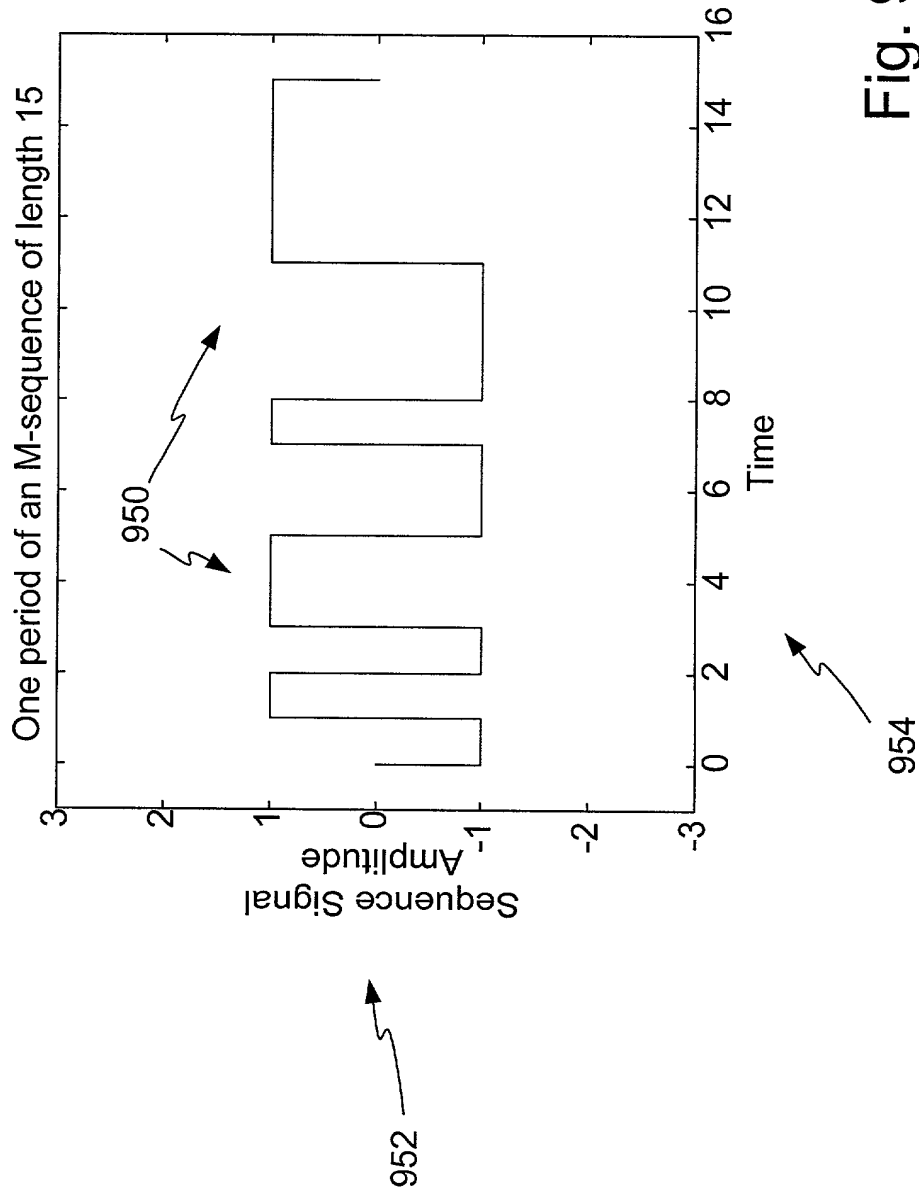


Fig. 9

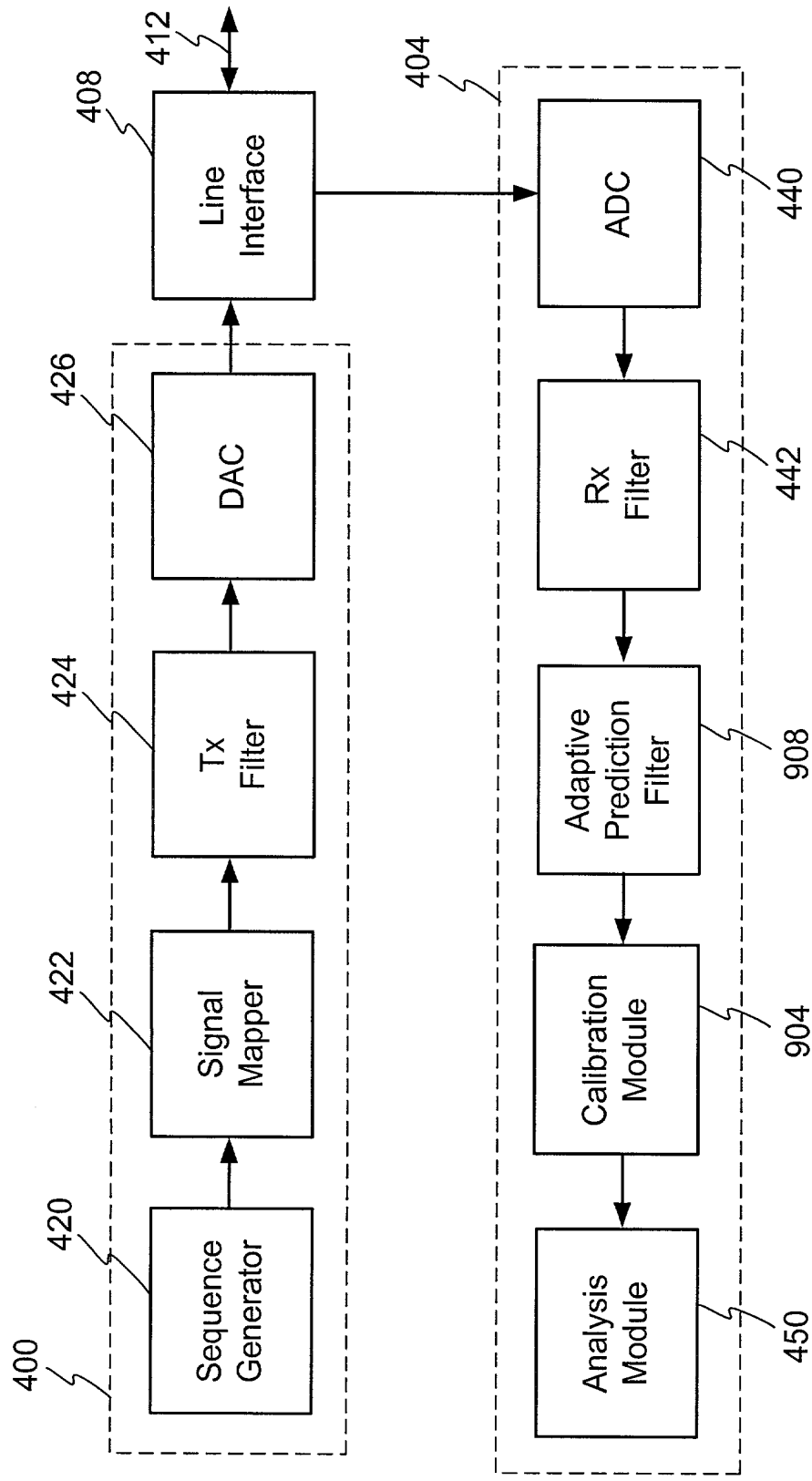


Fig. 10

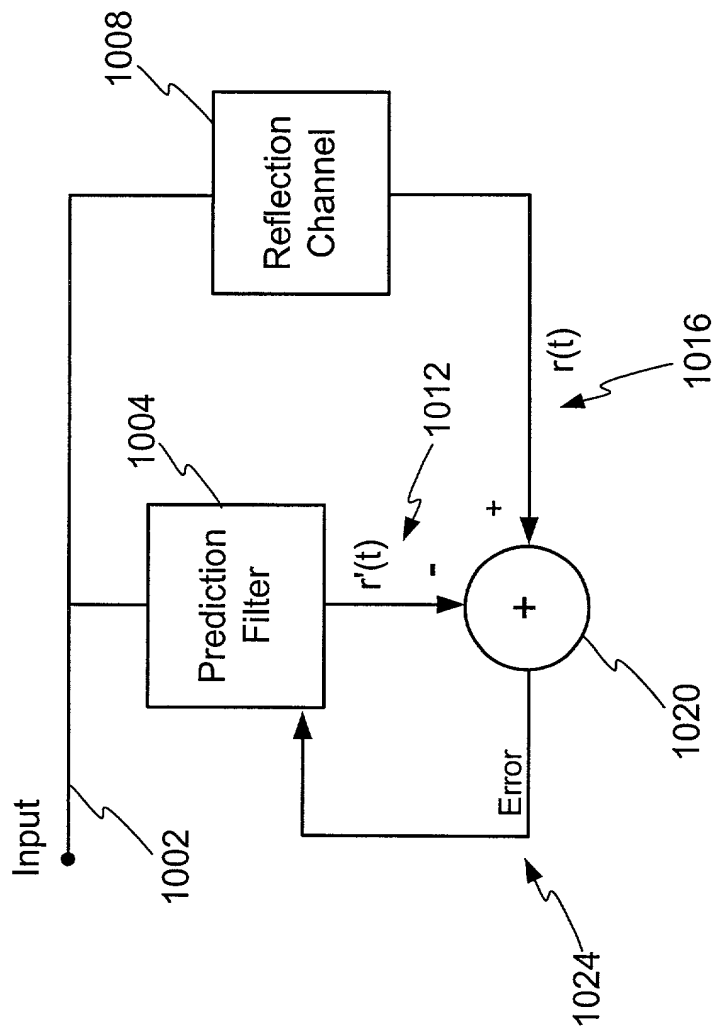


Fig. 11

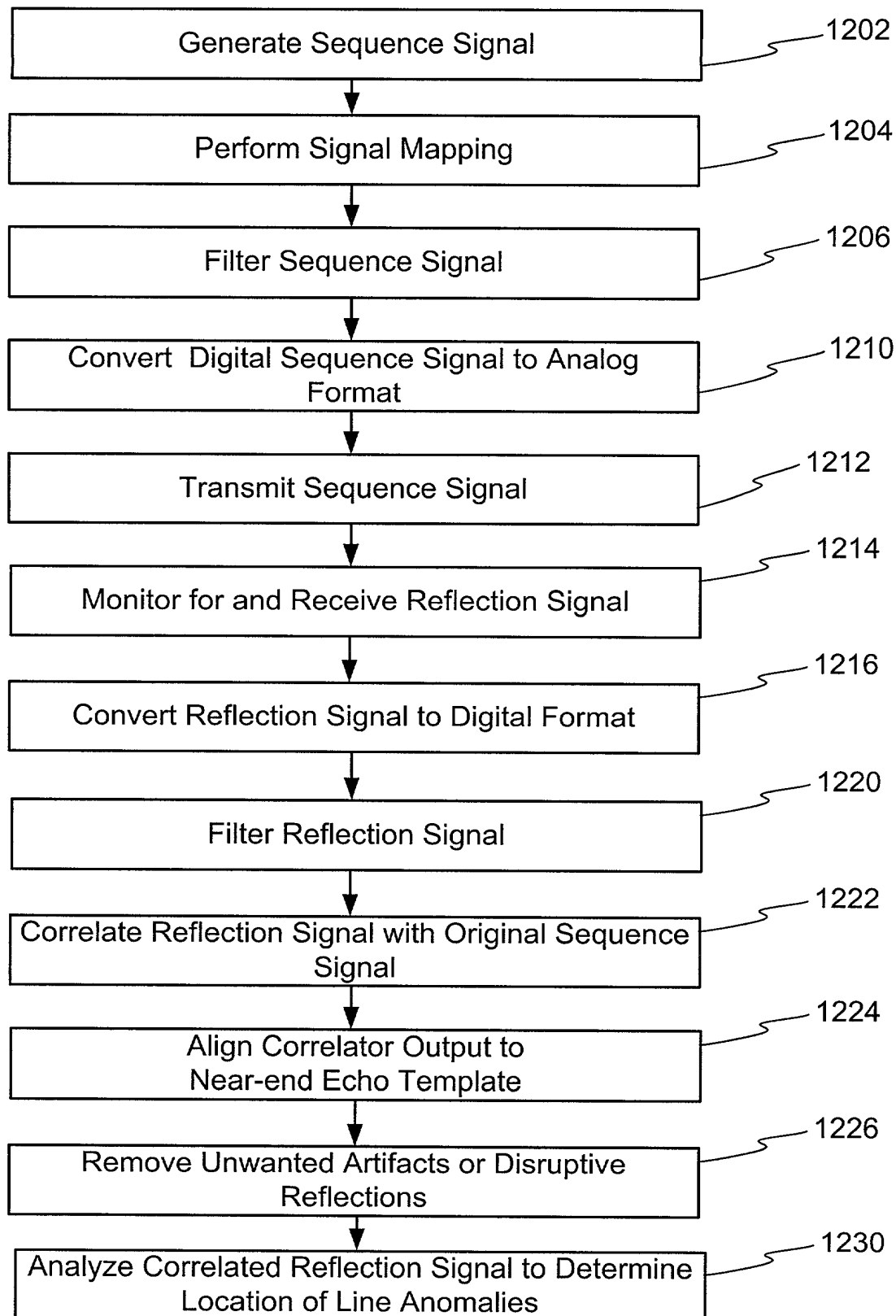


Fig. 12

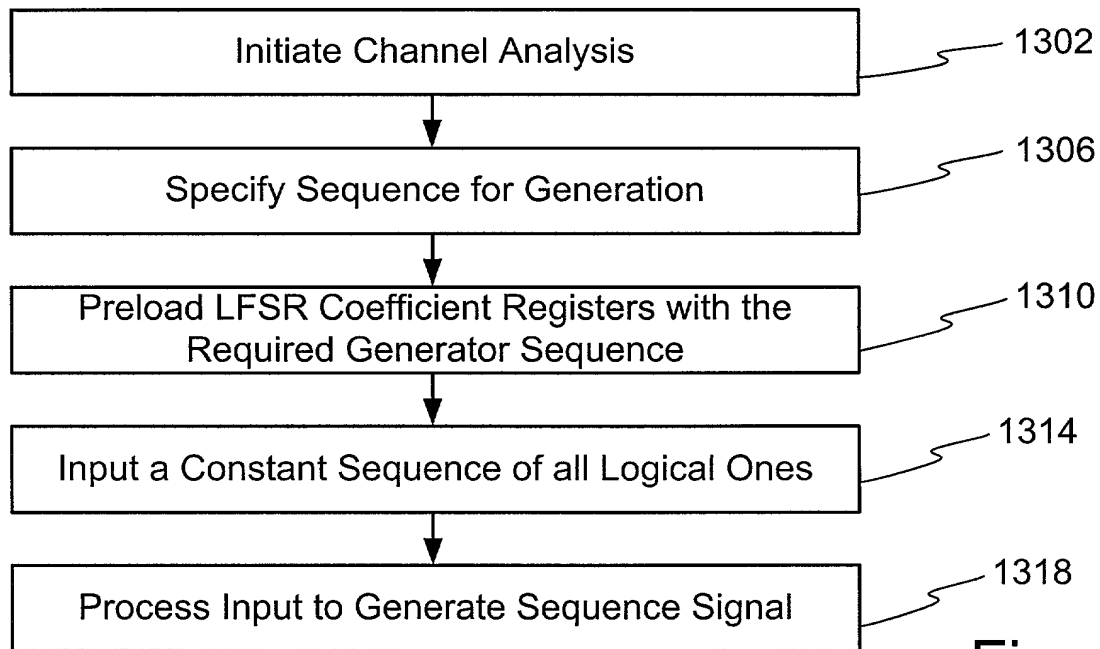


Fig. 13

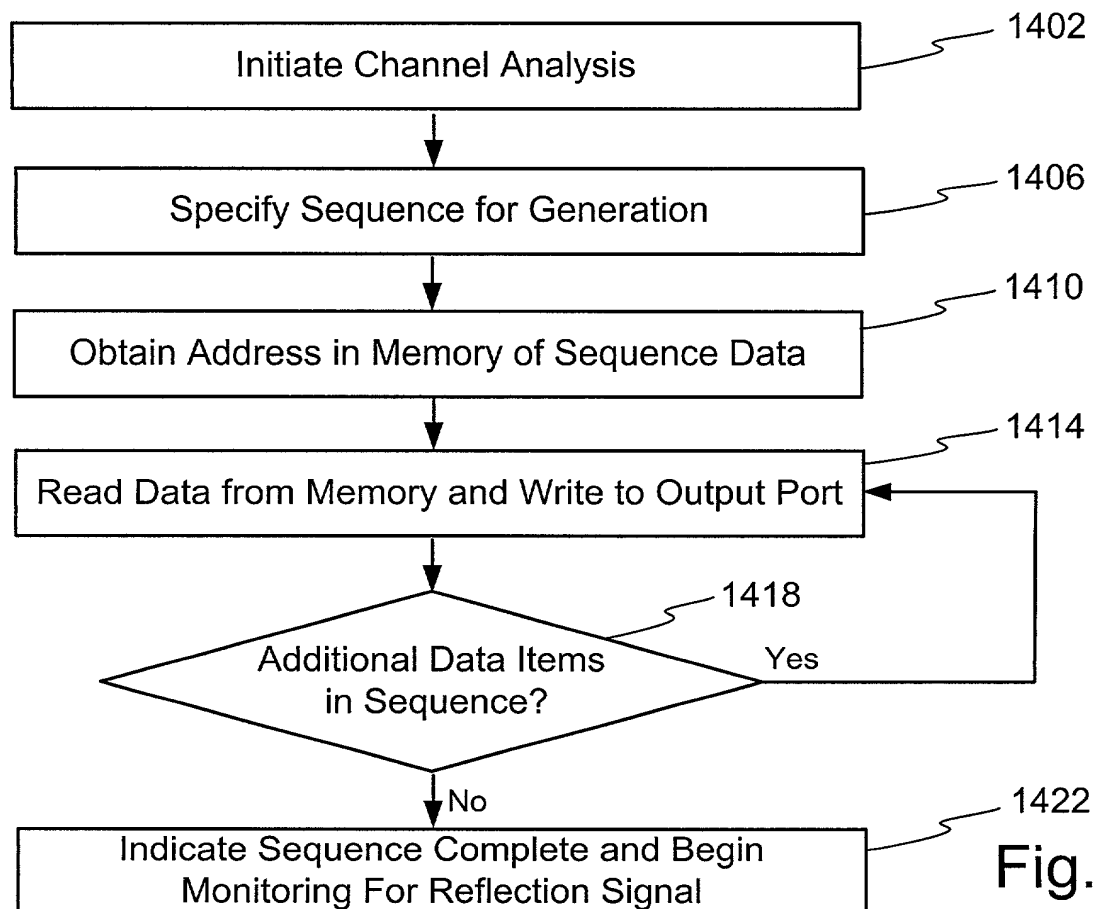


Fig. 14

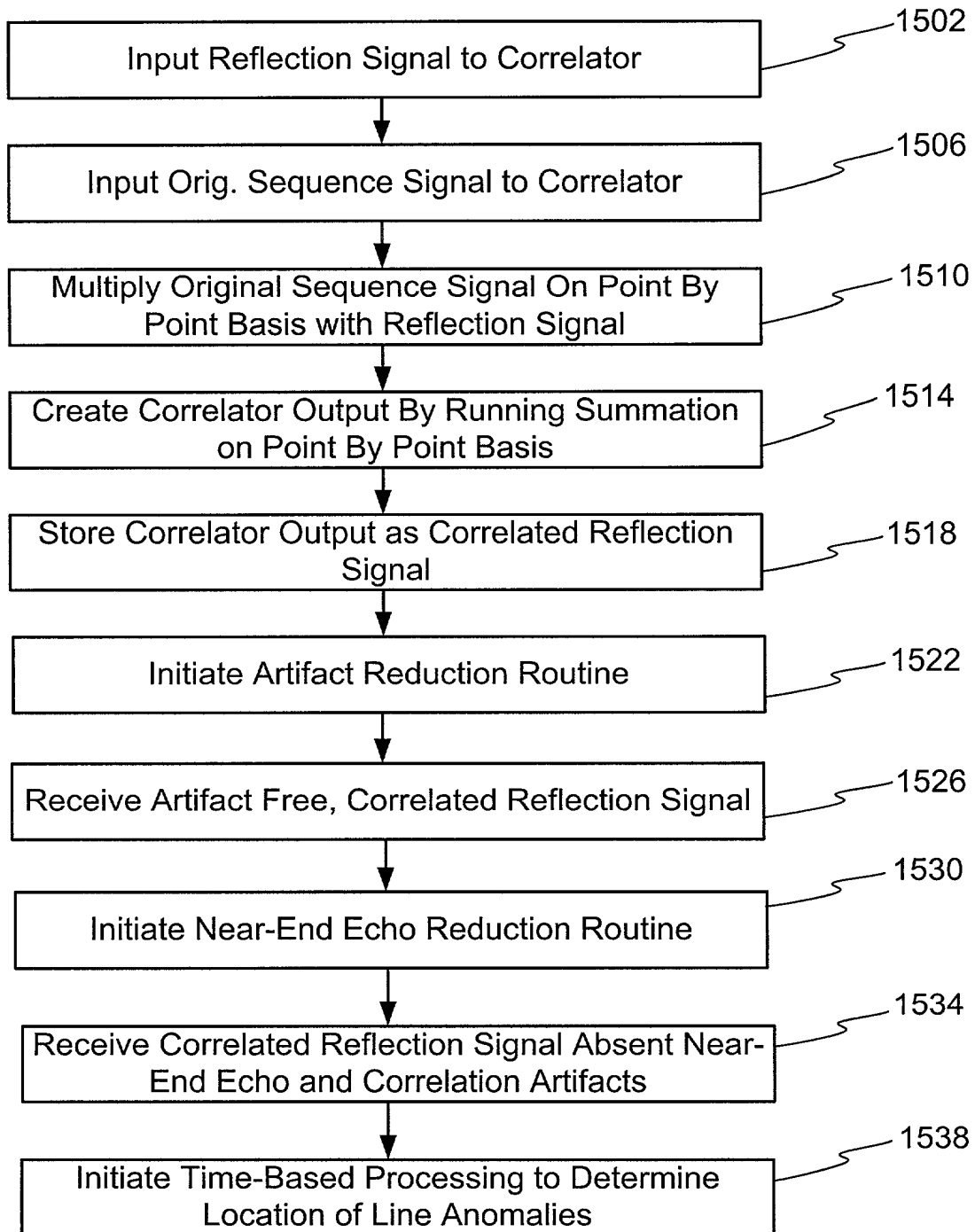


Fig. 15

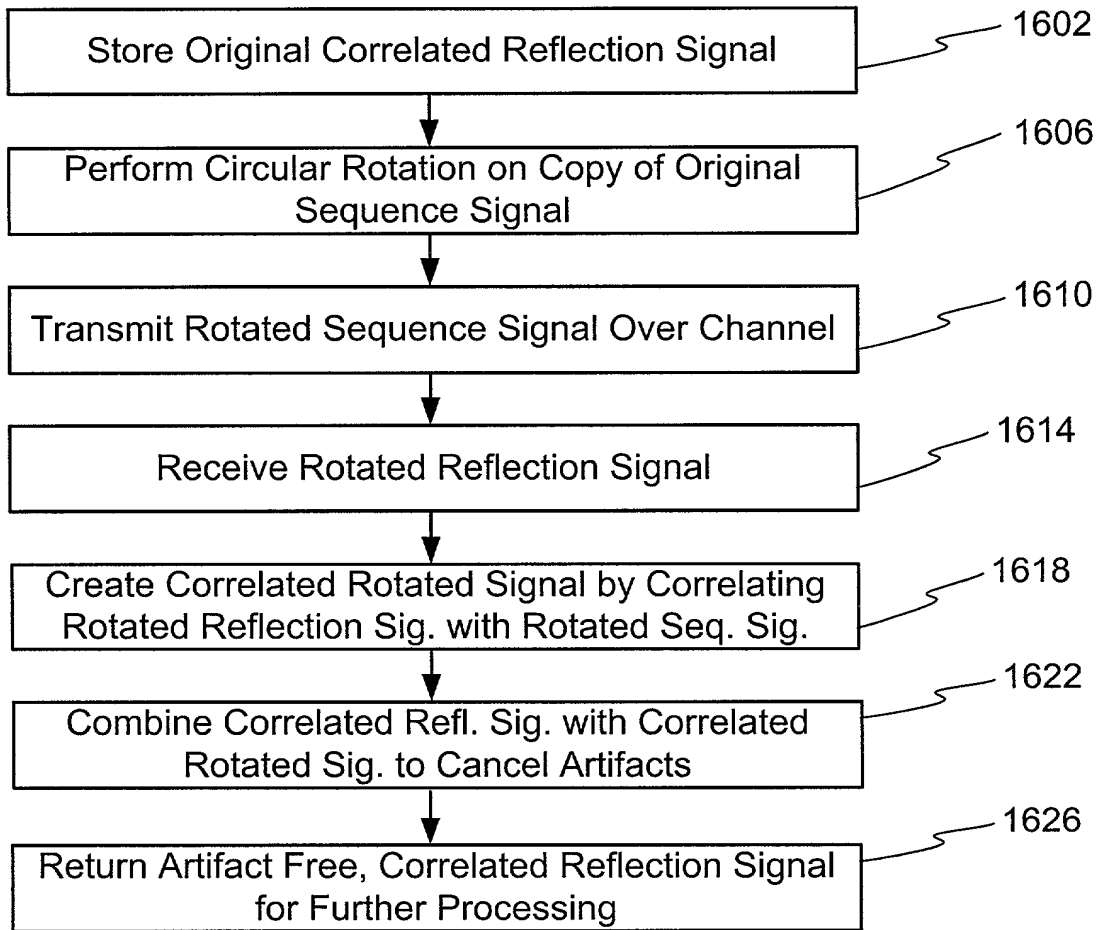


Fig. 16



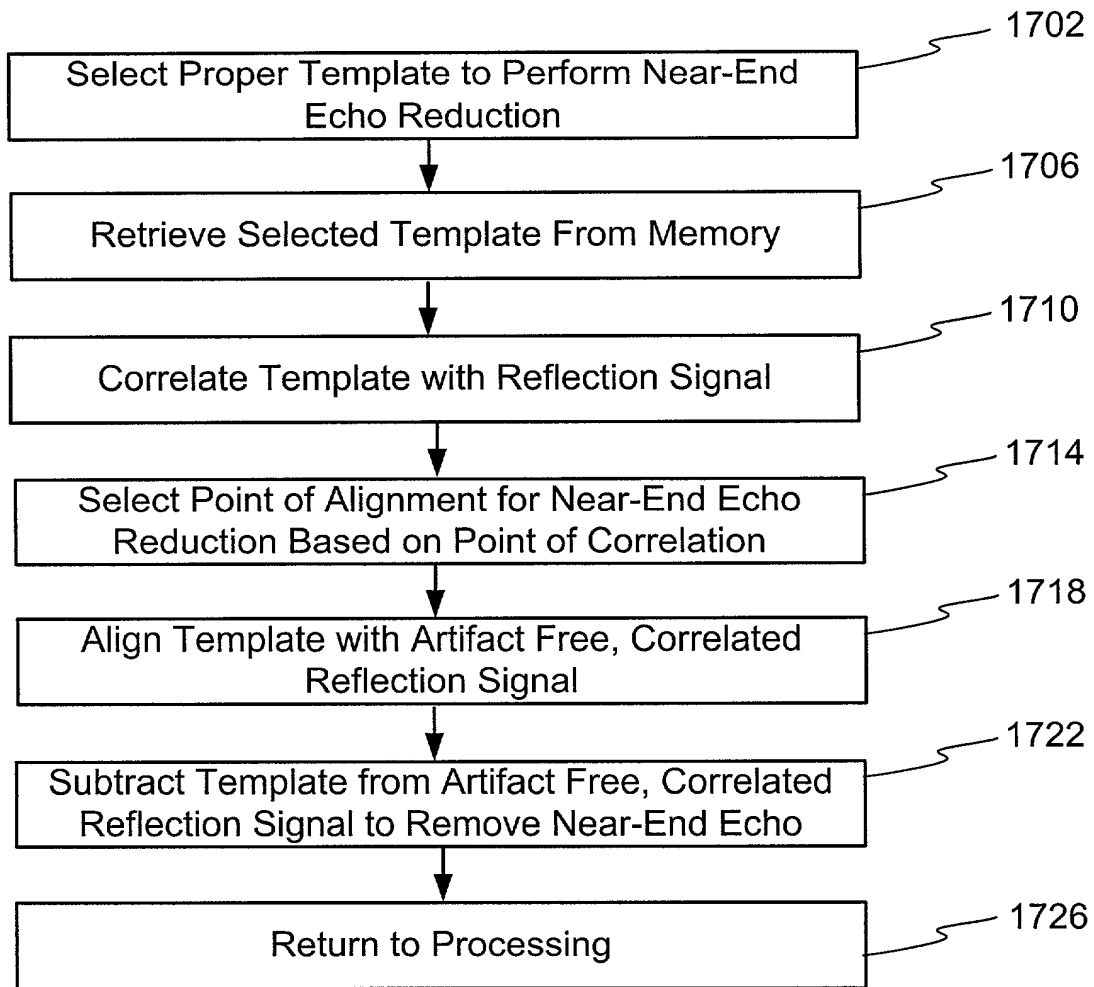


Fig. 17

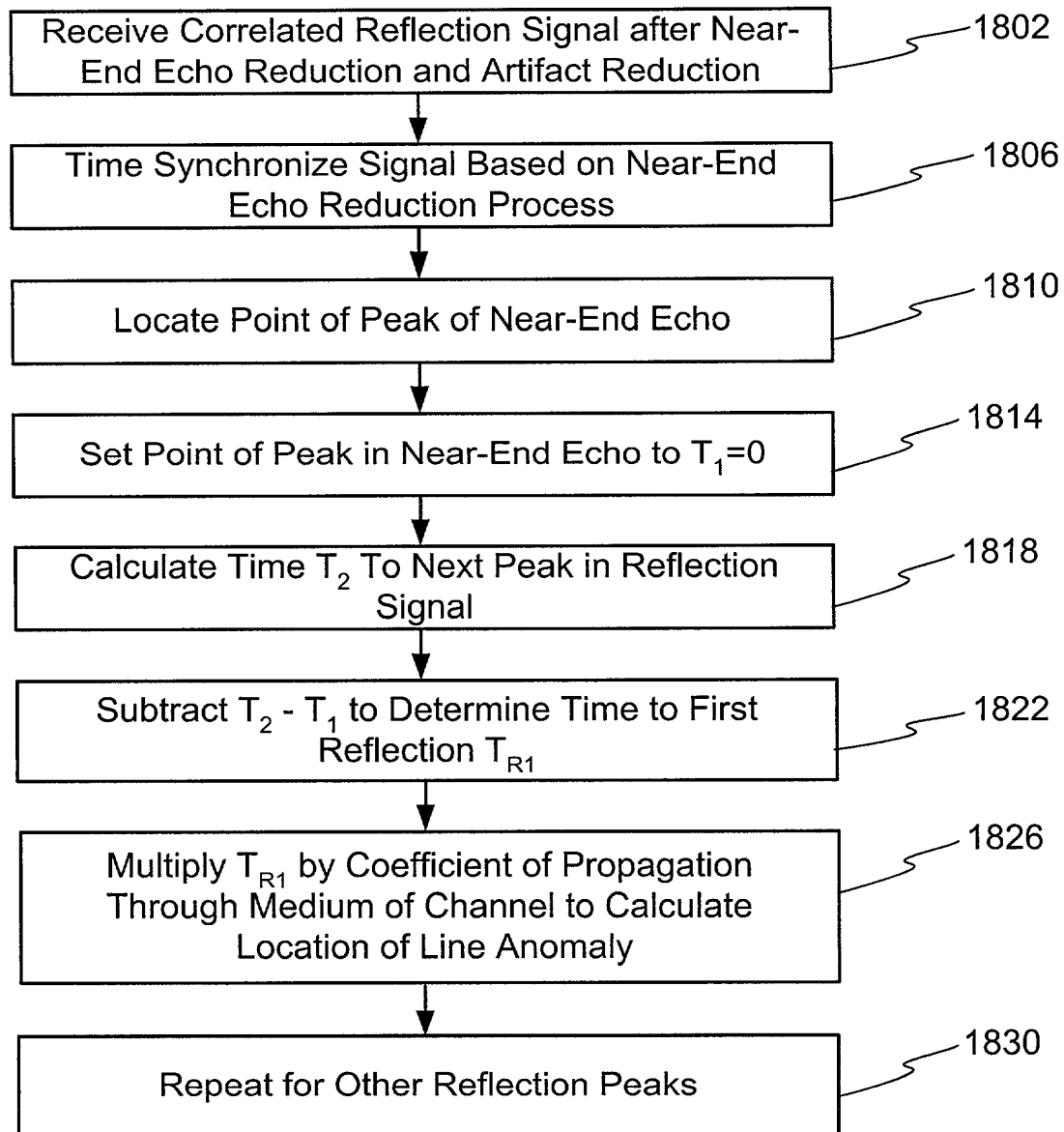


Fig. 18

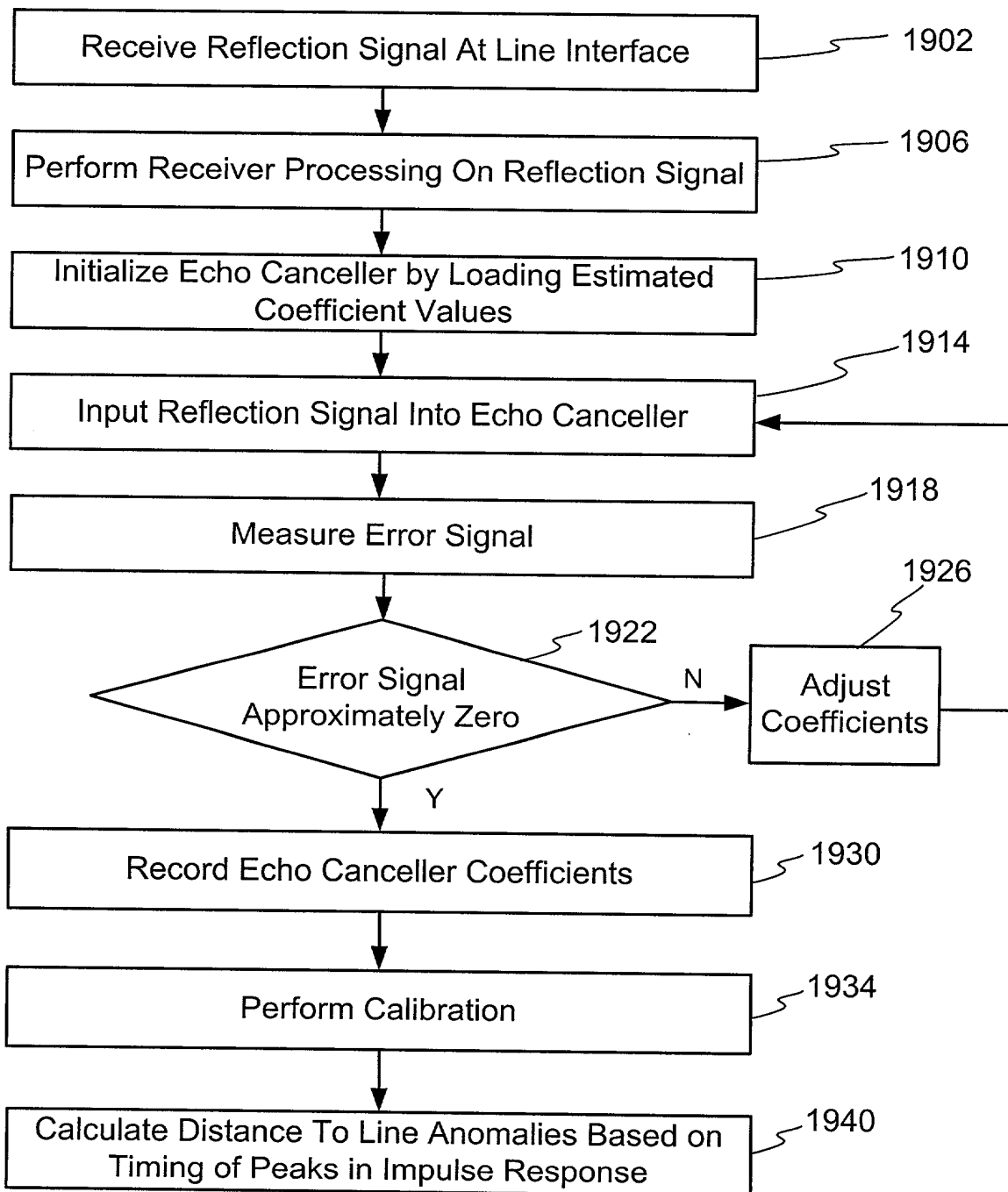


Fig. 19